

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	597	713/170.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 14:28
S7	2	713/170.ccls. and fpga and (hash digest MAC) and key	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 14:30
S8	9	713/170.ccls. and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 14:38
S9	52	713/170.ccls. and (pla or pld plc or asic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 14:40
S10	19	713/170.ccls. and (pla or pld plc or asic) and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 14:42
S11	20	713/170.ccls. and (fpga or "programable logic" pla or pld plc or asic) and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 14:44
S12	597	713/170.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 14:26
S13	3	(fpga "field programable" "gate array" "gated array" "programable chip" or "programable logic" pla or pld plc or asic) with key with (calculat\$3 hash\$3) with stor\$3 and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 14:48
S14	72	(fpga "field programable" "gate array" "gated array" "programable chip" or "programable logic" pla or pld plc or asic) with key with stor\$3 and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 14:48

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S17	1	(fpga "field programmable" "gate array" "gated array" "programmable chip" or "programmable logic" "programmable chip" pla or pld plc or asic) with key with stor\$3 same (hash digest "one way function" MAC signature) and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 17:09
S18	4	((("6694430") or ("5375169")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/04 15:06
S20	4	((("6694430") or ("5375169")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/04 17:04
S21	2	("6150837").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/04 17:05
S22	41	(fpga "field programmable" "gate array" "gated array" "programmable chip" or "programmable logic" "programmable chip" pla or pld plc or asic) same key same (hash digest "one way function" MAC signature) and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 12:00
S23	38	(fpga "field programmable" "gate array" "gated array" "programmable chip" or "programmable logic" "programmable chip" pla or pld plc or asic) same key same (hash digest "one way function" MAC signature) and memory and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/04 17:09
S24	545	(fpga "field programmable" "gate array" "gated array" "programmable chip" or "programmable logic" "programmable chip" pla or pld plc or asic) and key same (hash digest "one way function" MAC signature) and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 12:40
S25	4	((("5748738") or ("5727061")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/05 12:40

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S26	41	(fpga "field programmable" "gate array" "gated array" "programmable chip" or "programmable logic" "programmable chip" pla or pld plc or asic) same key same (hash digest "one way function" MAC signature) and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 12:40
S27	504	S24 not S26	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 12:40
S28	184	S27 and (programing programmable programed) and ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 12:41
S29	89	S27 and (programing programmable programed) and ASIC and ("nonvolatile" "non-volatile")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 12:45
S30	28	S27 and (programing programmable programed) same ASIC and ("nonvolatile" "non-volatile")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 14:23
S31	1311	326/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 14:23
S32	866	326/39.ccls. and @ay <="1999"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 14:23
S34	710	326/39.ccls. and @ay <="1999" and (fpga "field programable" "gate array" "gated array" "programable chip" or "programable logic" pla or pld plc or asic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 14:24
S36	527	713/165.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/05 14:26